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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Tadahiro KURODA, et al.

Serial Number: Not Yet Assigned

(§371 of International Application PCT/JP05/02117)

Filed: August 8, 2006

For: ELECTRONIC CIRCUIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

August 8, 2006

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449 and cited in the enclosed international search report. References AE-AF and AJ-AL are cited in the international search report.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. <u>01-2340</u>.

Respectfully submitted,

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Enclosures: PTO-1449; References (5); International Search Report

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	Applicant(s): Tadahiro KURODA, et al.		
PTO-1449	Filing Date: August 8, 2006	Group Art Unit: Not Yet Assigned	

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class		Filing Date (If _ appropriate)
	AA AB				SPOASIOOZOASIOASIOASIOASIOASIOASIOASIOASIOASI	907000510.6900500500500500500500500500	
200701000000000000000000000000000000000	AC AD		SESSORIO ESPASSO ASSOCIATA				

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		Document No.	Date	Country	Translation (Yes or No)
/Y.S./	AE	2000-124406	04/28/00	Japan	Yes-Abstract
/Y.S./	AF	11-68033	03/09/99	Japan	Yes-Abstract/Discussed in the specification
	AG				
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OTHER DOCUMENTS

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/Y.S./	AK	Kyokai Society Taikai Koen Ronbunshu; September 10, 2003; p. 3. K. Kanda, et al.; "1.27 Gb/s/pin, 3mW/pin Wireless Superconnect (WSC) Interface Scheme;" The Institute of Electronics, Information and Communication Engineers
	AL	Gijutsu Kenkyu Hokoku; Vol. 103; No. 88 (ICD2003-16); May 2003; pp. 19-22. K. Kanda, et al.; "2003 IEEE International Solid-State Circuits Conference ISSCC 2003/ Sesion 10/ High Speed Building Blocks/Paper 10.7; Vol. 1; 2003; (10 Sheets.)
Examiner	/	Yuriy Semenenko/ Date Considered 02/23/2009